

Figure 1: Prior art - Typical Active Matrix Display

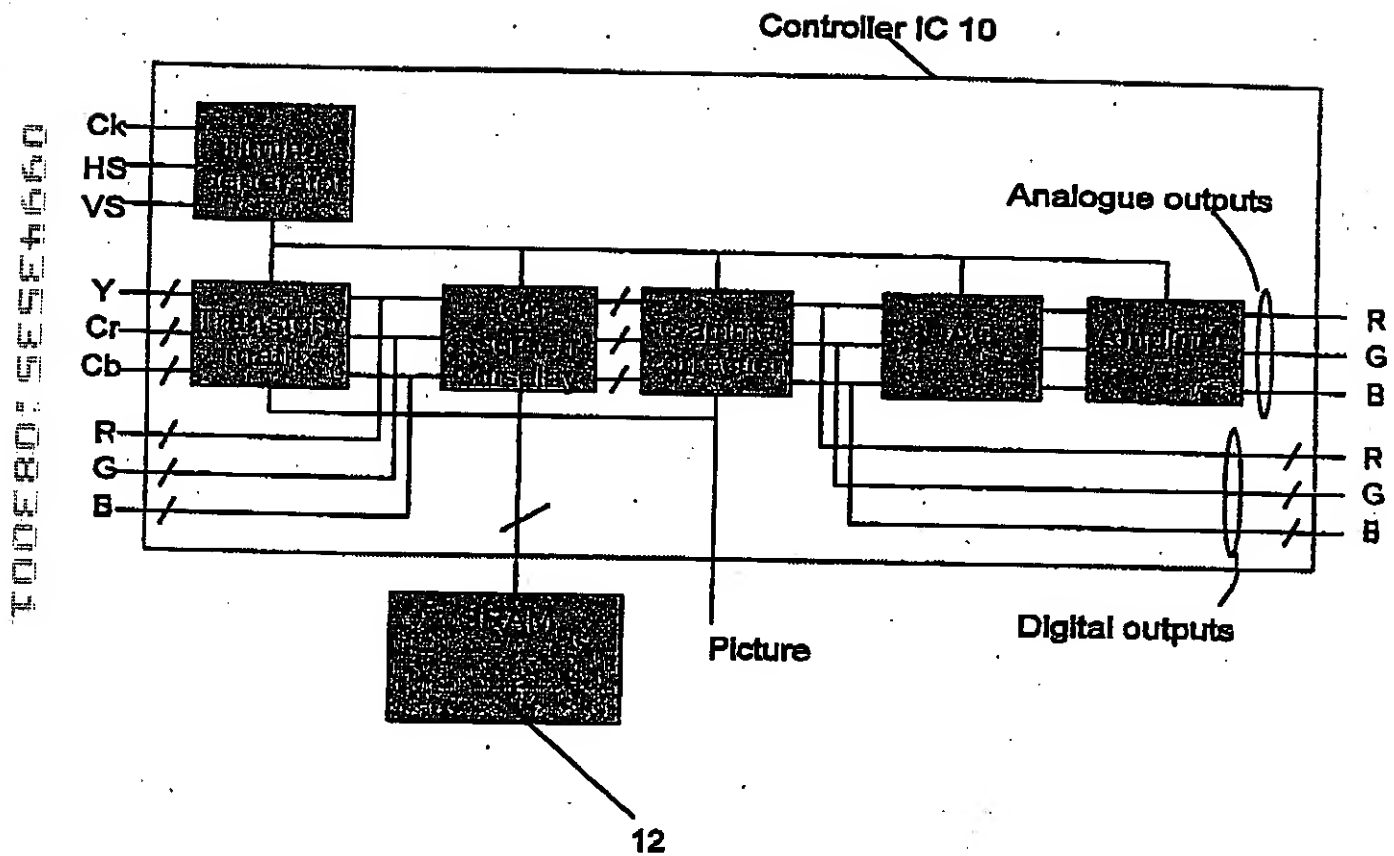


Figure 2 prior art - typical active matrix display controller IC

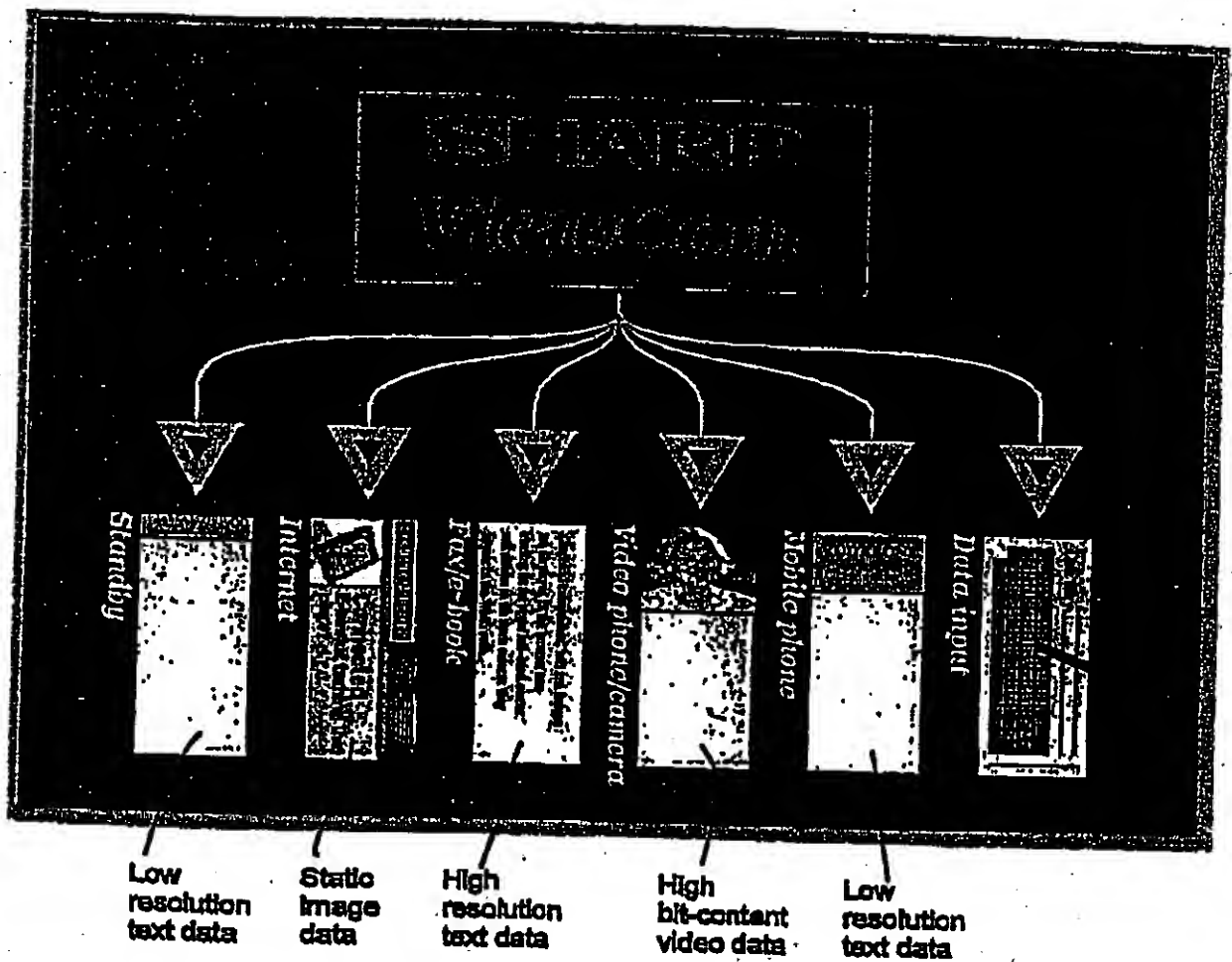


Figure 3: Conceptual application of multi-format image data

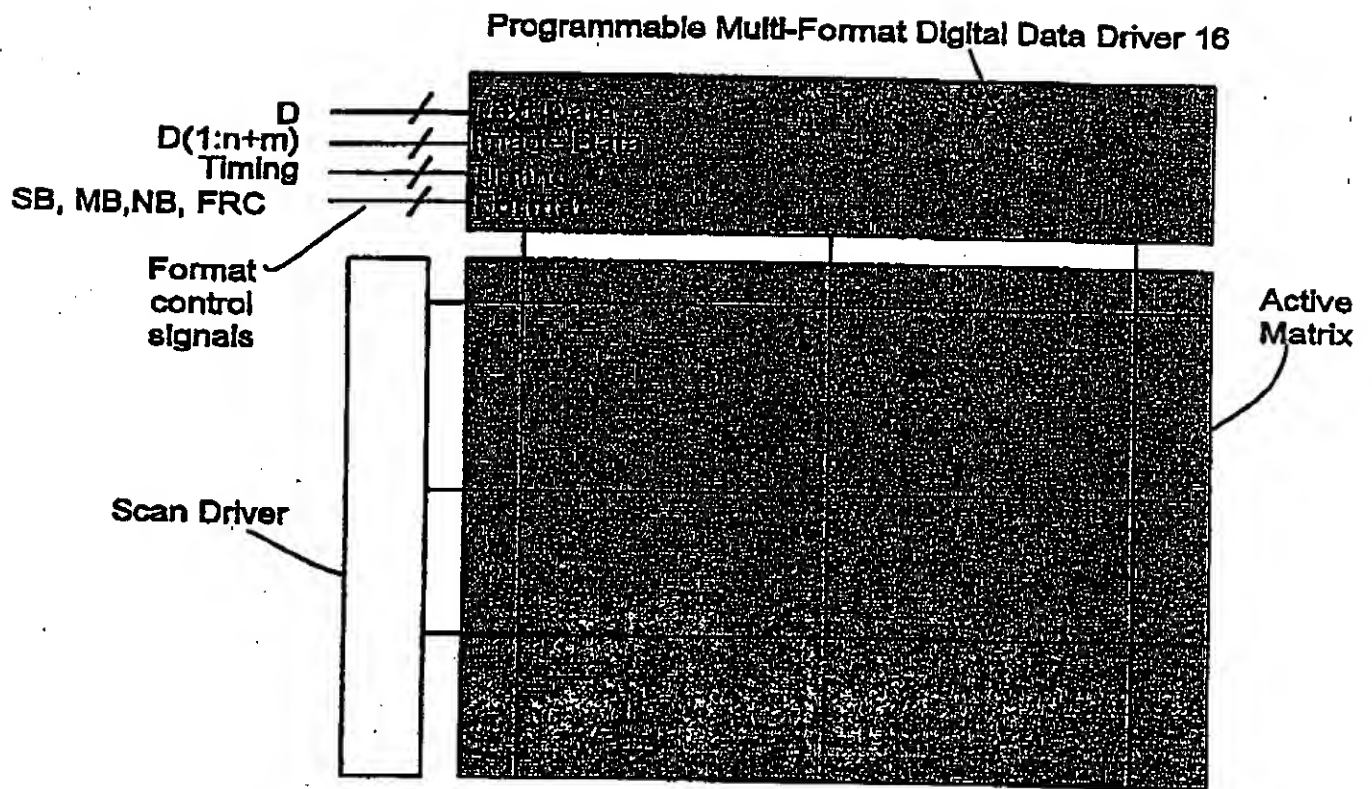


Figure 4: Multi-format digital data driver

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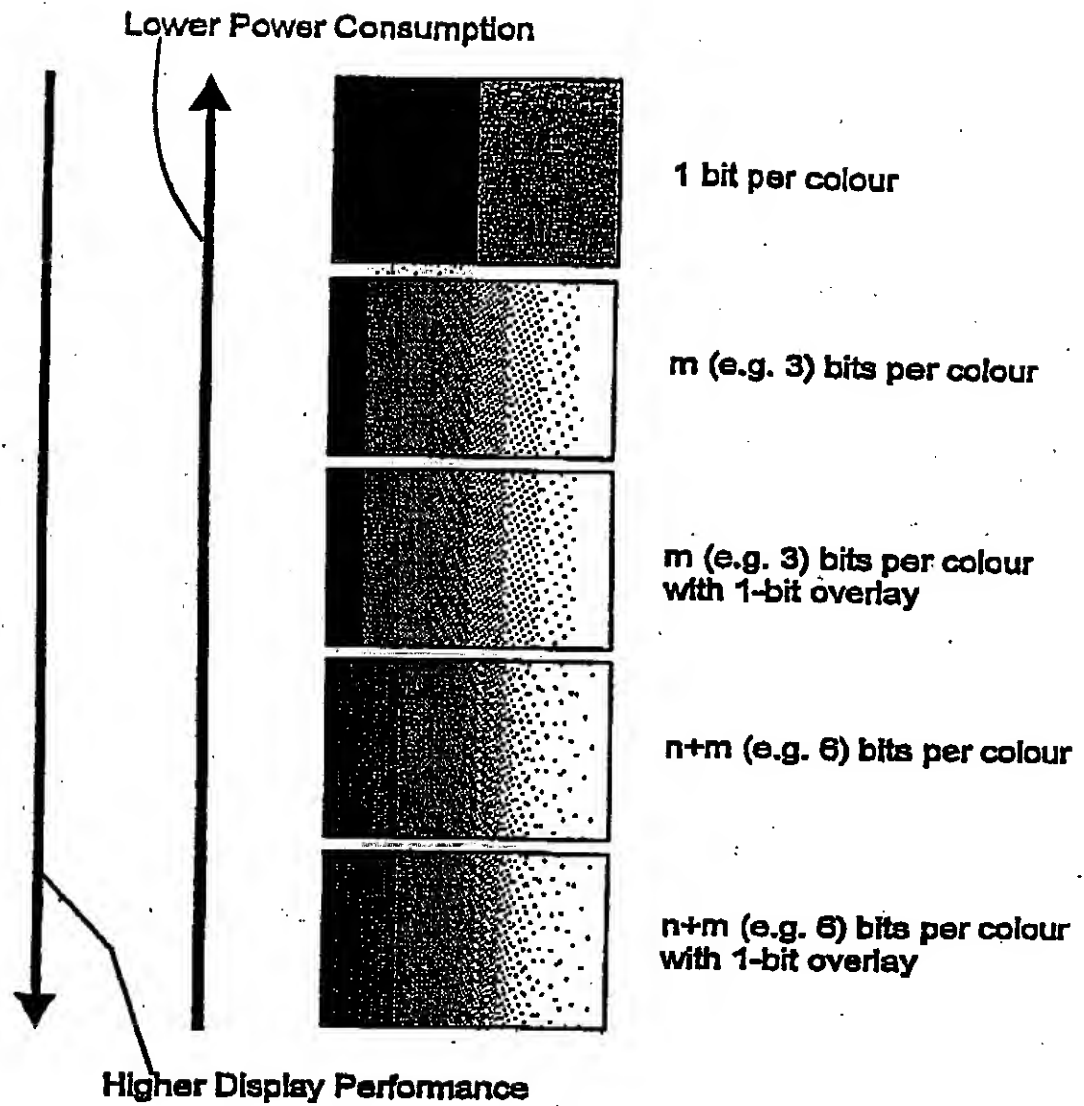


Figure 5: The trade-off between power consumption and image quality

Format Control			
NB	MB	SB	Driver Format
0	0	1	1-bit per colour
0	1	0	m bits per colour
1	1	0	n+m bits per colour
0	1	1	m bits per colour with 1-bit overlay
1	1	1	n+m bits per colour with 1-bit overlay

Figure 6: Table showing three Format Control Signals and selected format

09943535:083001

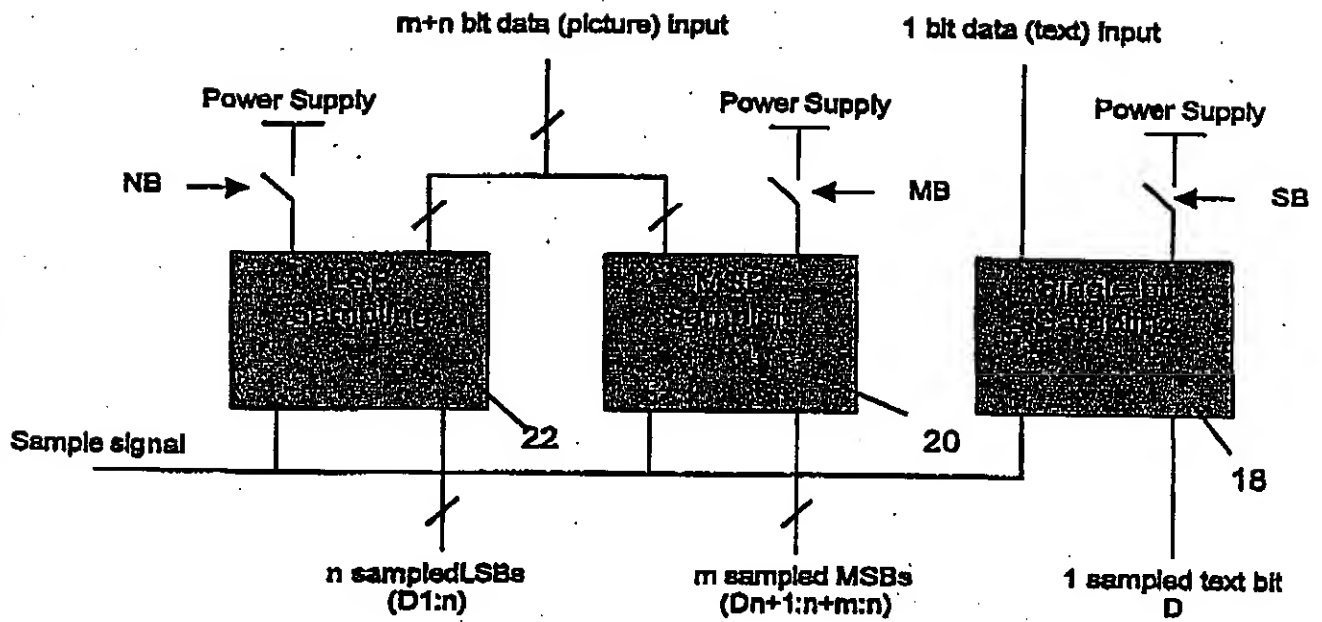


Figure 7: Power control within data sampling unit of multi-format driver

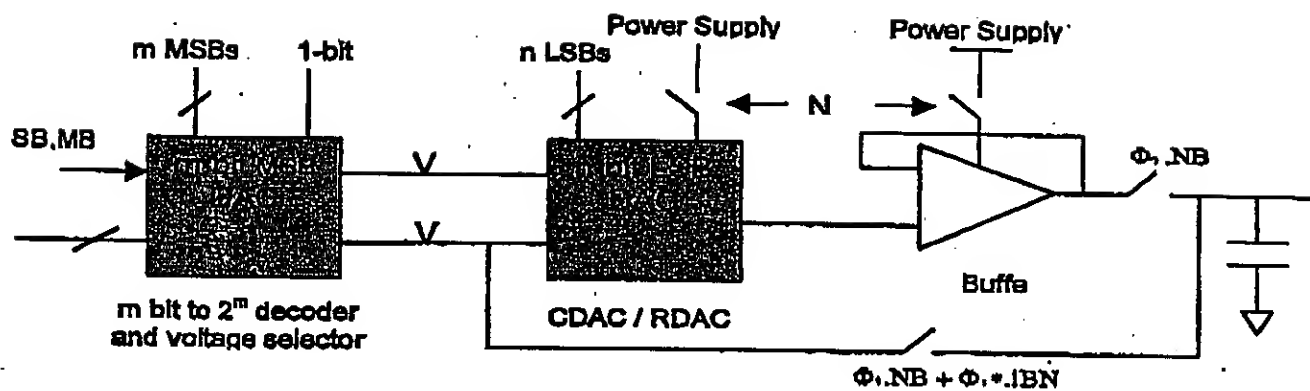


Figure 8 Variable resolution DAC with power control

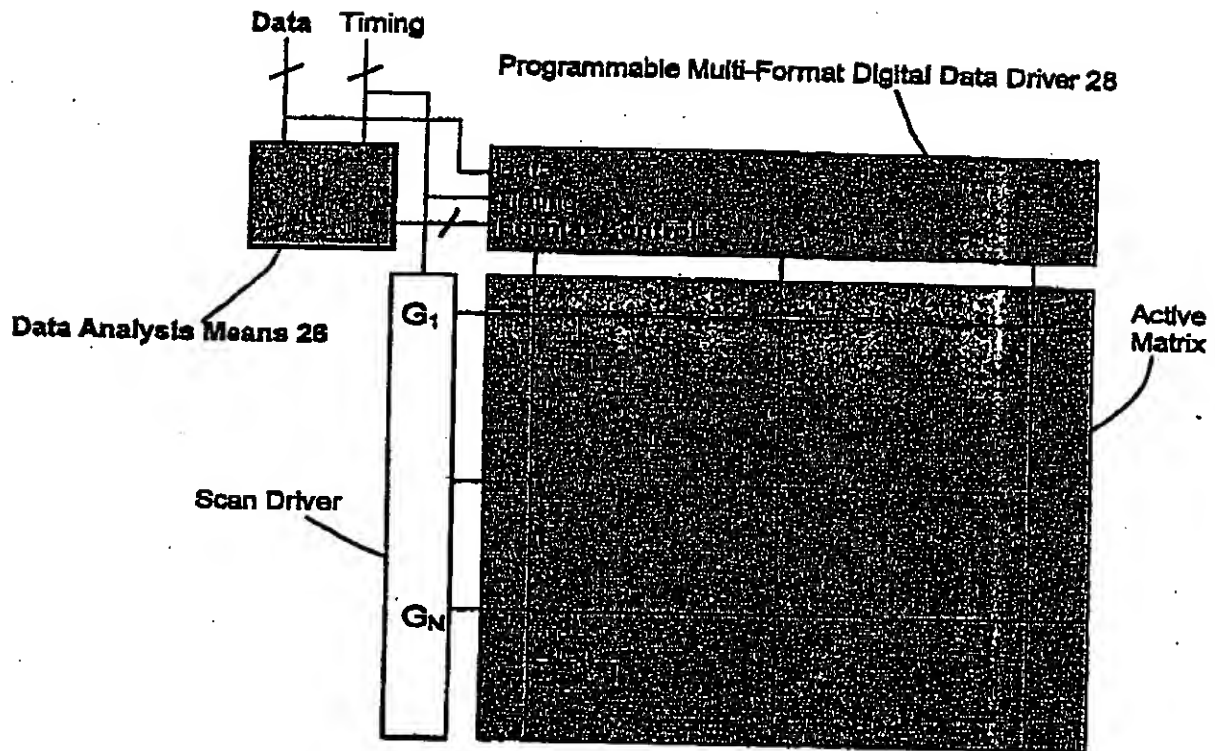


Figure 9 Basic embodiment of the invention — content driven format control

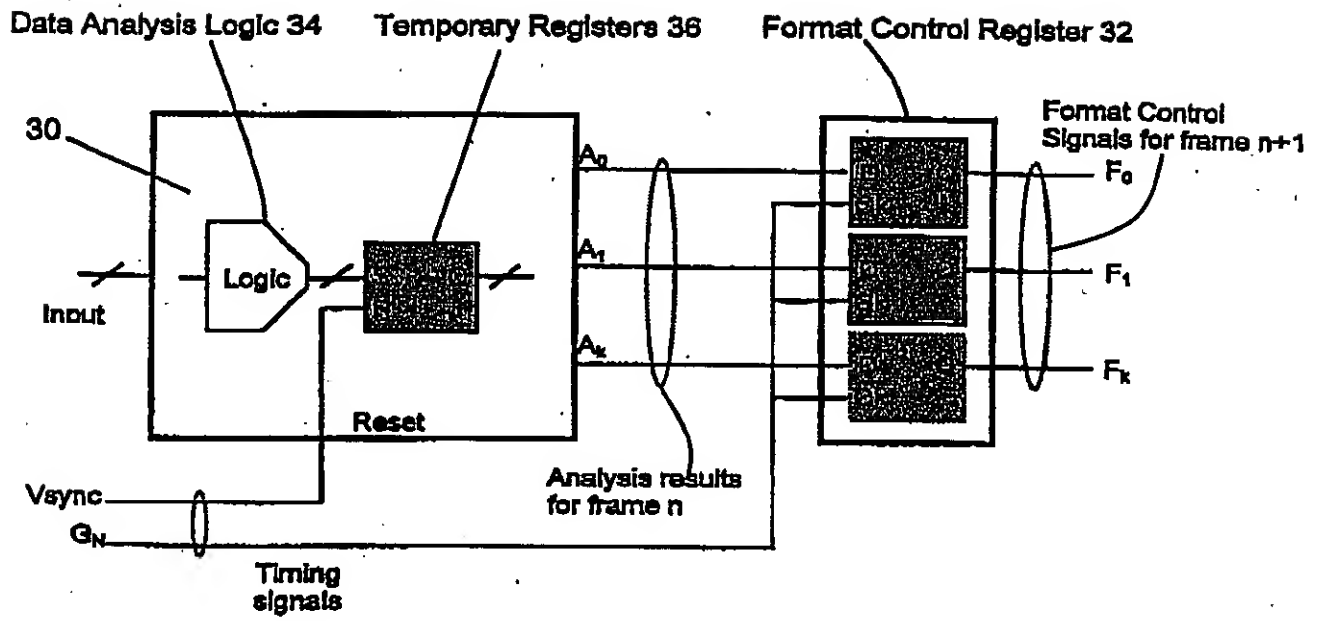


Figure 10 The functional blocks of the data analysis means

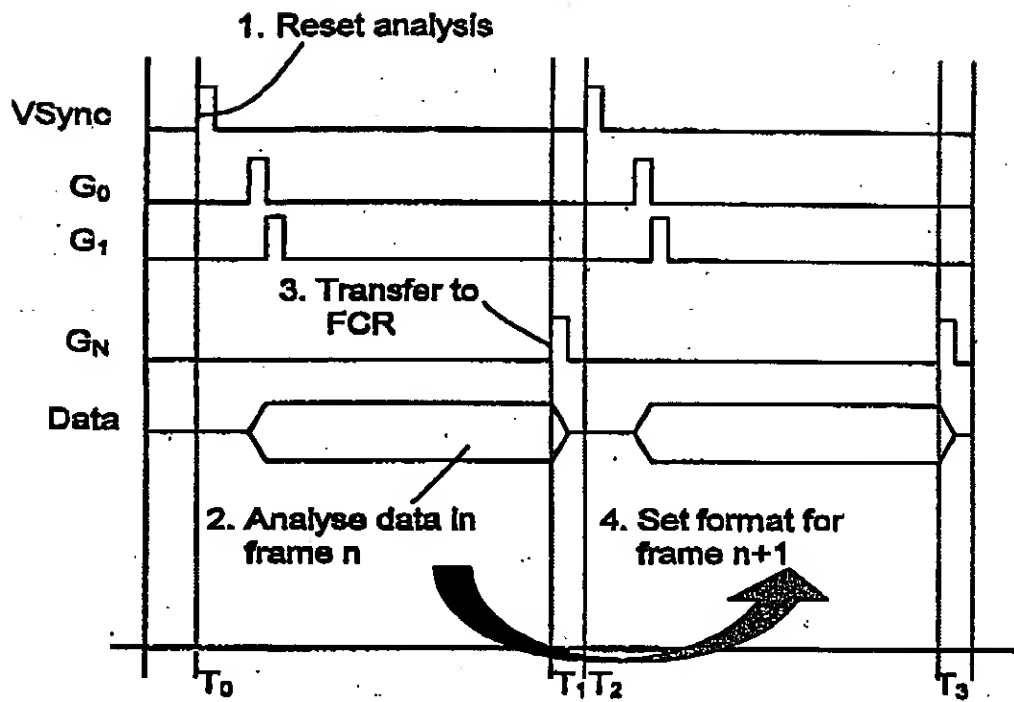


Figure 11 Generalised timing diagram for data analysis means

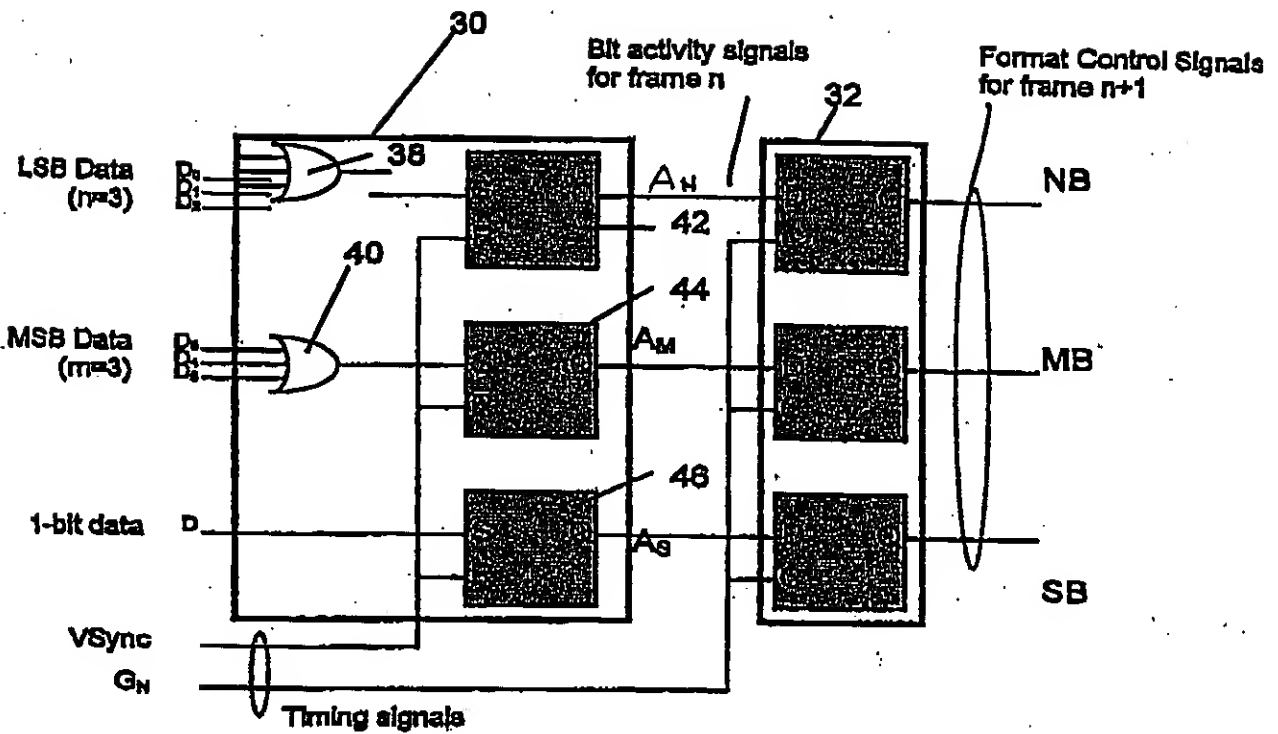


Figure 12 Embodiment of Invention – generation of bit-resolution control signals

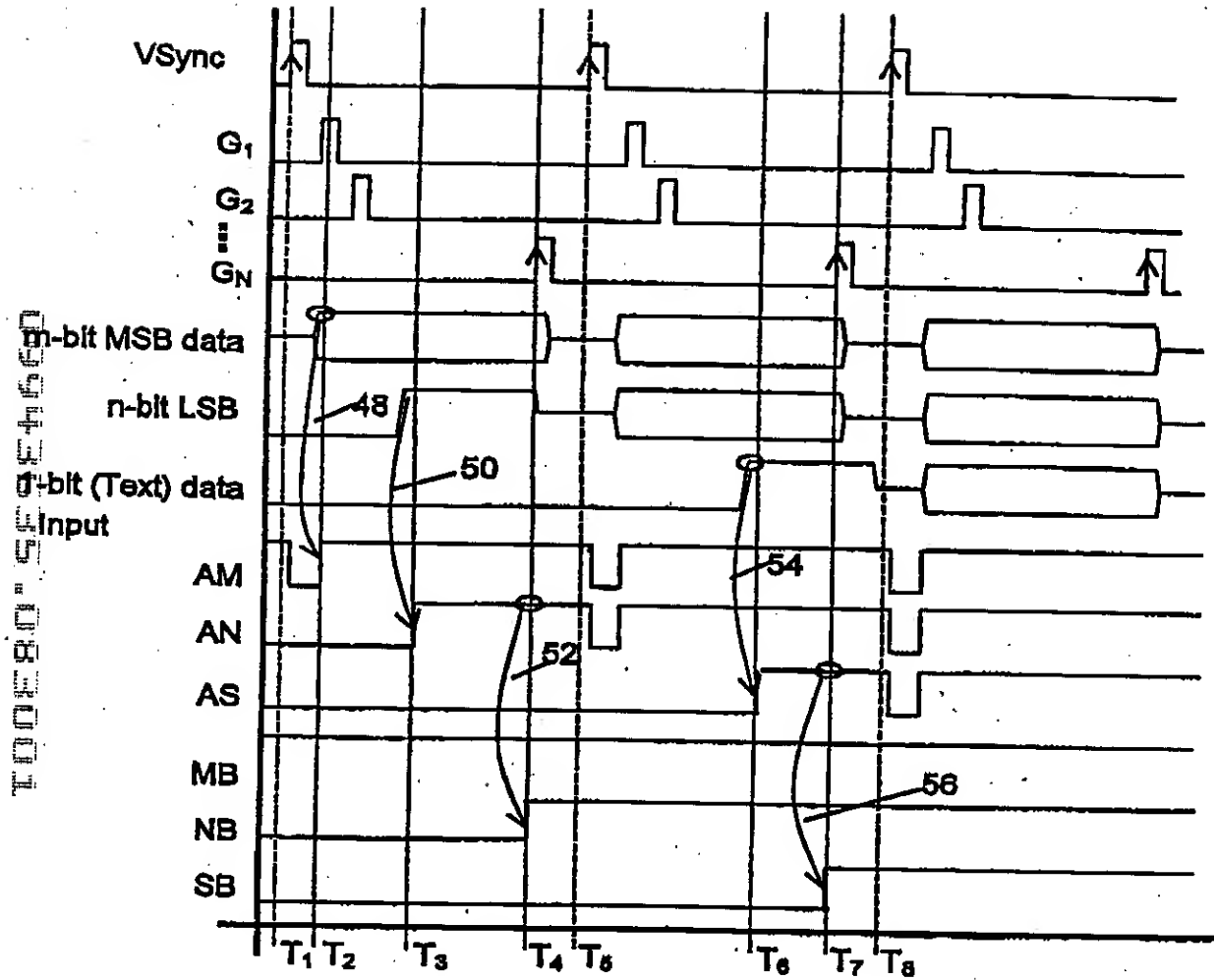


Figure 13: Timing diagram for bit-resolution embodiment

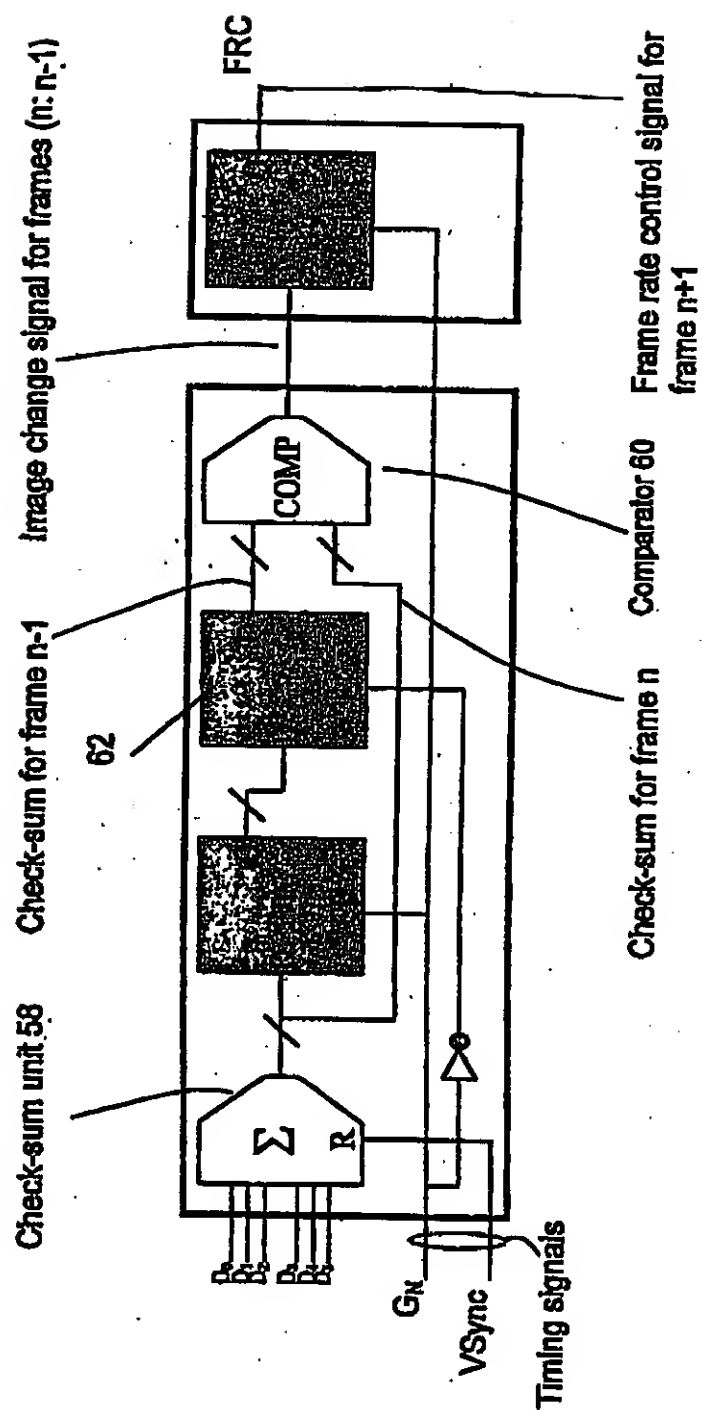


Figure 14: Embodiment of invention – analysis means for static image detection

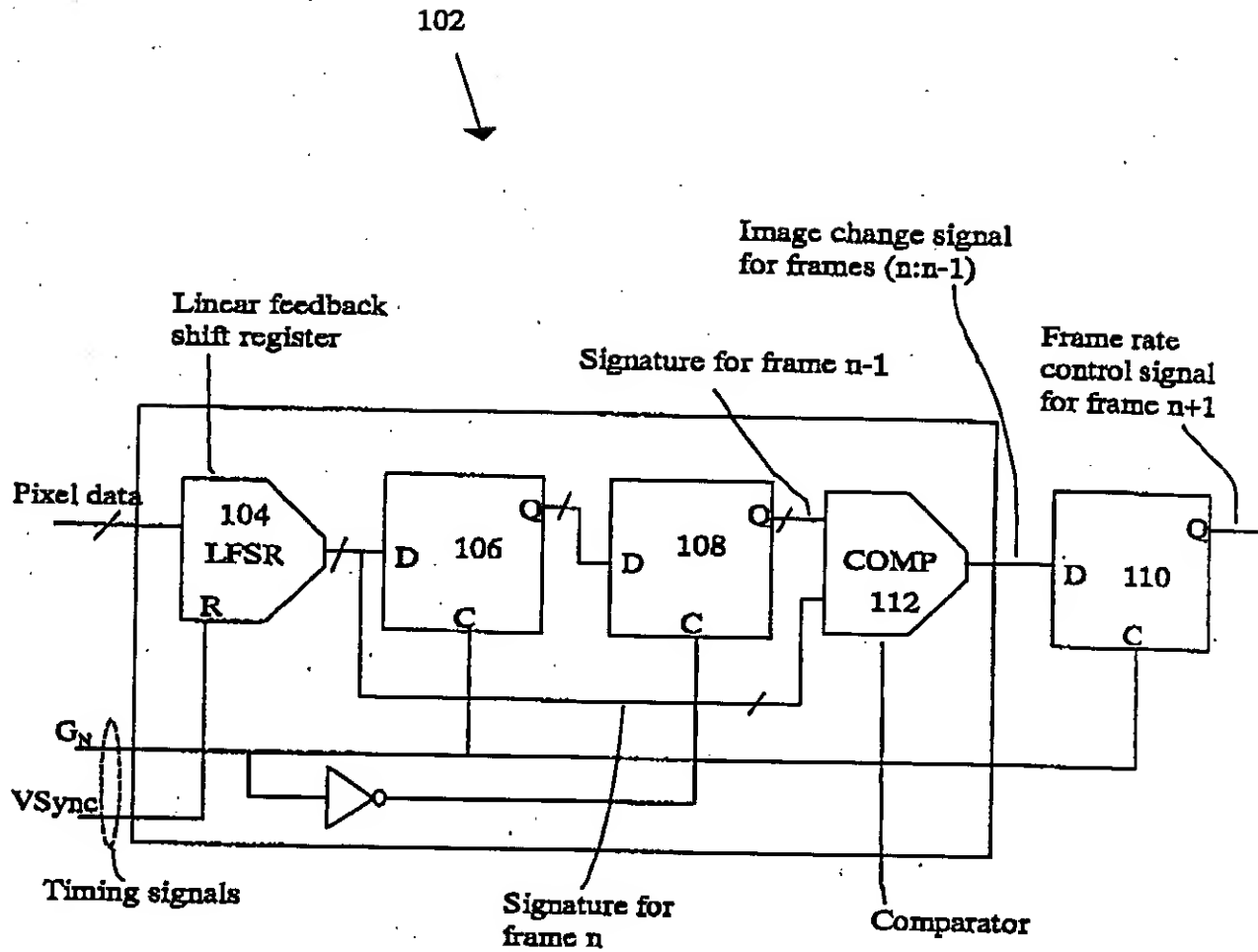


Figure 15

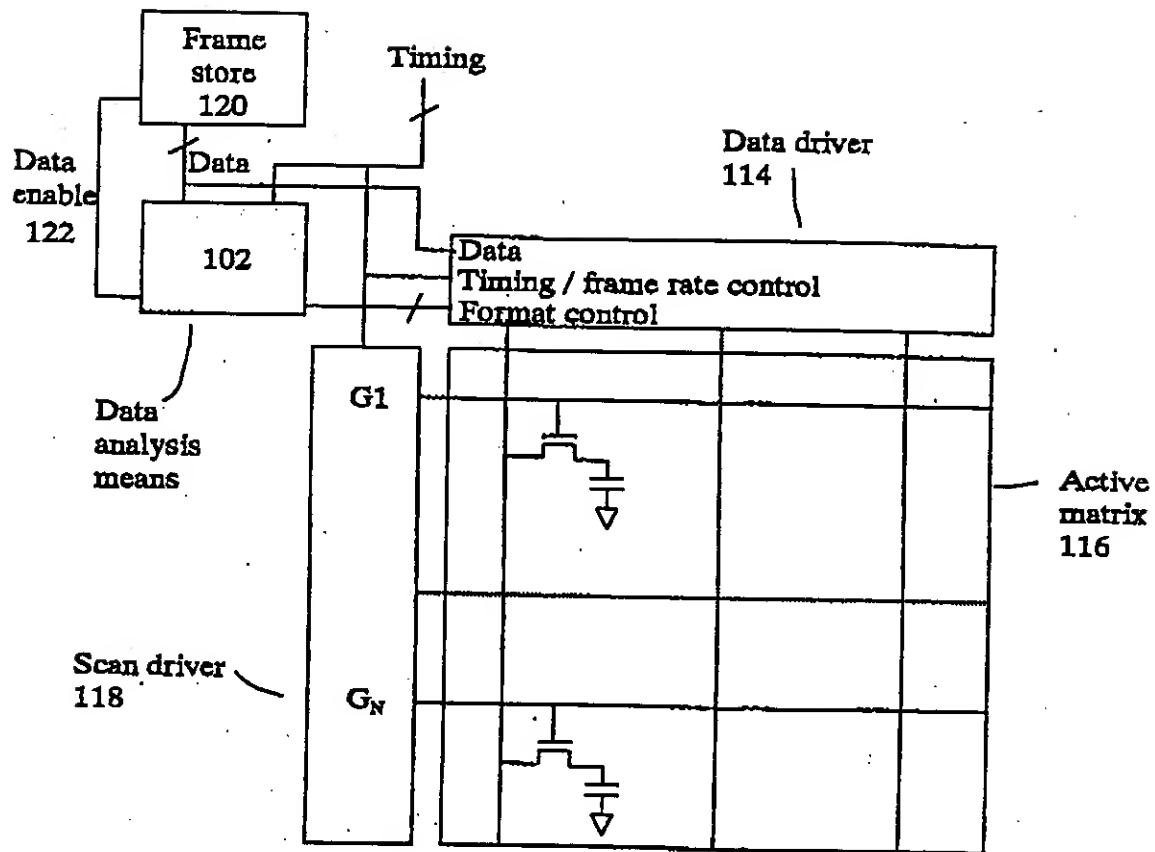


Figure 16



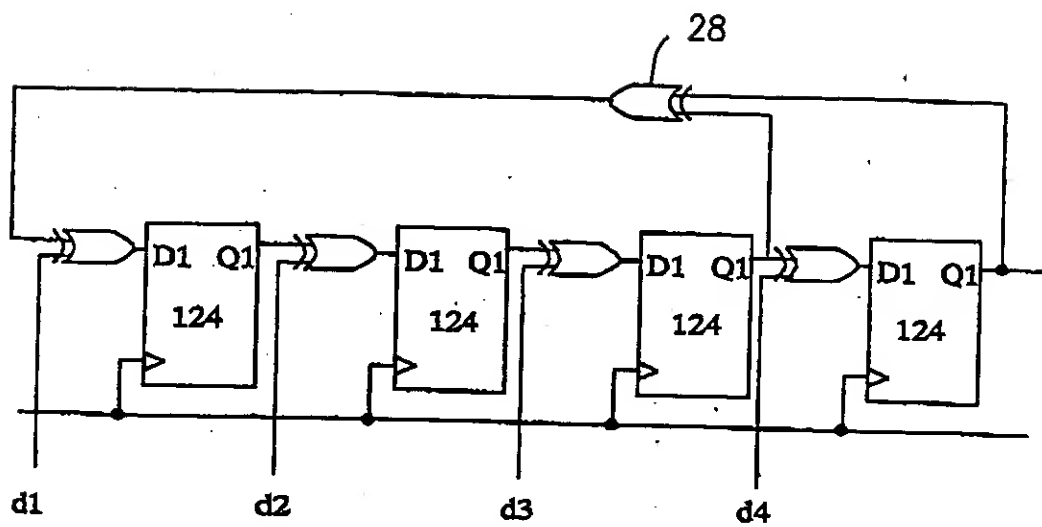


Figure 18

T00E80-SESEH560

Figure 19

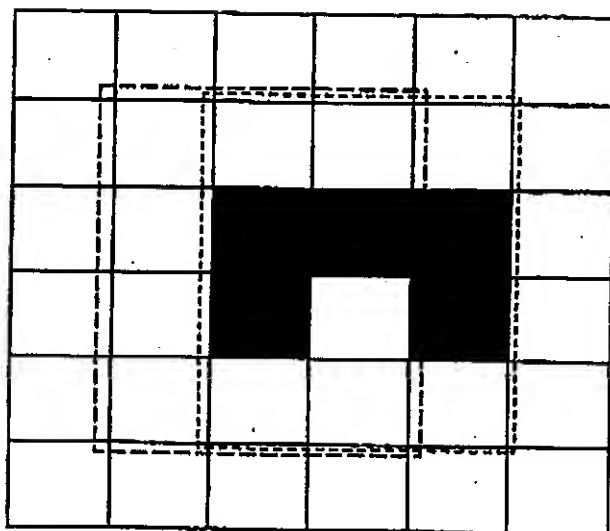
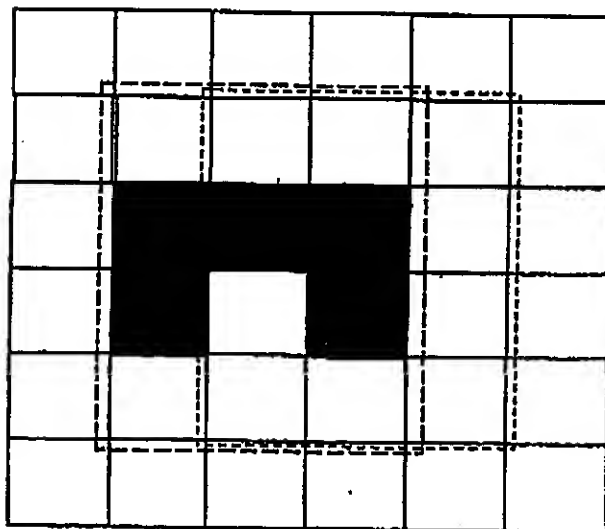


Figure 20



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